

Erik J. Welsh

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Rice University:
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OBJECTIVE: To obtain summer field experience in electrical engineering processes, with an emphasis on computer hardware/software development.

EDUCATION: **Rice University** Houston, Texas
BSEE expected 2001 MEE expected 2002 GPA: 3.77 Credit: 106 hours
Course Work: Introduction to Electrical Engineering, Signals and Systems, Digital Logic Design with Lab, Computer Architecture, Electronic Circuits, VLSI, DSP, Physical Electronics, Lego Lab, Introduction to Principles of Scientific Computation, Intermediate Computer Programming, Applied Algorithms and Data Structures, Waves and Optics, Partial and Ordinary Differential Equations, Complex Analysis, Statistics and Vector Calculus.

L.B.J. High School Science Academy Austin, Texas
Graduation: 1998 GPA: 4.68 Class Rank: 1 (1998)
Studies included: CADD, Manufacturing, Chemistry, Biology, Calculus, Calculus based Physics, Differential Equations, Pascal Programming, Multi-variable Calculus and Linear Algebra.

WORK

EXPERIENCE: **Software Engineer**
Tivoli System Inc. Austin, Texas Summer 2000
Researched using GCC as a cross platform compiler as a means to improve the existing Tivoli software build process. Participated in verification exercises. Wrote scripts in Perl to perform desired tasks. Learned the basics of Solaris Administration.

Laboratory Instructor / Teaching Assistant Fall 1999, Spring 2000,
Rice University Houston, Texas Fall 2000
Led lab sections of Intermediate Computer Programming and Introduction to Electrical Engineering, presenting information and answering questions about Java and circuit theory.

Information System Technician
Travis County Austin, Texas Summer 1999
Assisted in management and maintenance of the County's computer network. Troubleshoot problems with hardware, software, and printers. Learned basic network administration of a Novell, token-ring network.

SKILLS:
Computer Languages: Java, Perl, Make, C, C++, and Scheme
Computer Programs: Win95, WinNT, UNIX, Matlab, Powerview, Generic CADD, MS Word, Excel
EE Fundamentals: Digital Logic Design using MSI and SSI, Control Systems, Circuit Theory and Signal Theory, DSP, VLSI

AWARDS

RECEIVED: High School Science Academy Valedictorian
National Merit Finalist
National Honor Society
Barbara Jordan Essay Contest Regional Finalist and NCTE nominee.
Kodak Young Scholar Award for academic achievement.
W. L. Moody Engineering Scholarship
Abell-Hanger Foundation Scholarship through the Texas Interscholastic League Foundation